



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Gordon Ray NELSON et al.

Serial No.: 09/612,009

Filed: July 7, 2000

For: Automated Semiconductor Processing  
System

Group Art Unit: 3652

Examiner: T. Brahan

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GROUP 360

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing in the above-referenced application are formal drawings

(37 sheets).

Respectfully submitted,  
LYON & LYON LLP

Dated: November 13, 2001

By:

*Kenneth H. Ohrner*  
Kenneth H. Ohrner  
Reg. No. 31,646



22249

LYON & LYON LLP  
Suite 4700  
633 W. Fifth Street  
Los Angeles, CA 90071  
(213) 489-1600

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(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

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*Maria D. Santos*  
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